



RX

10

3D

1.1

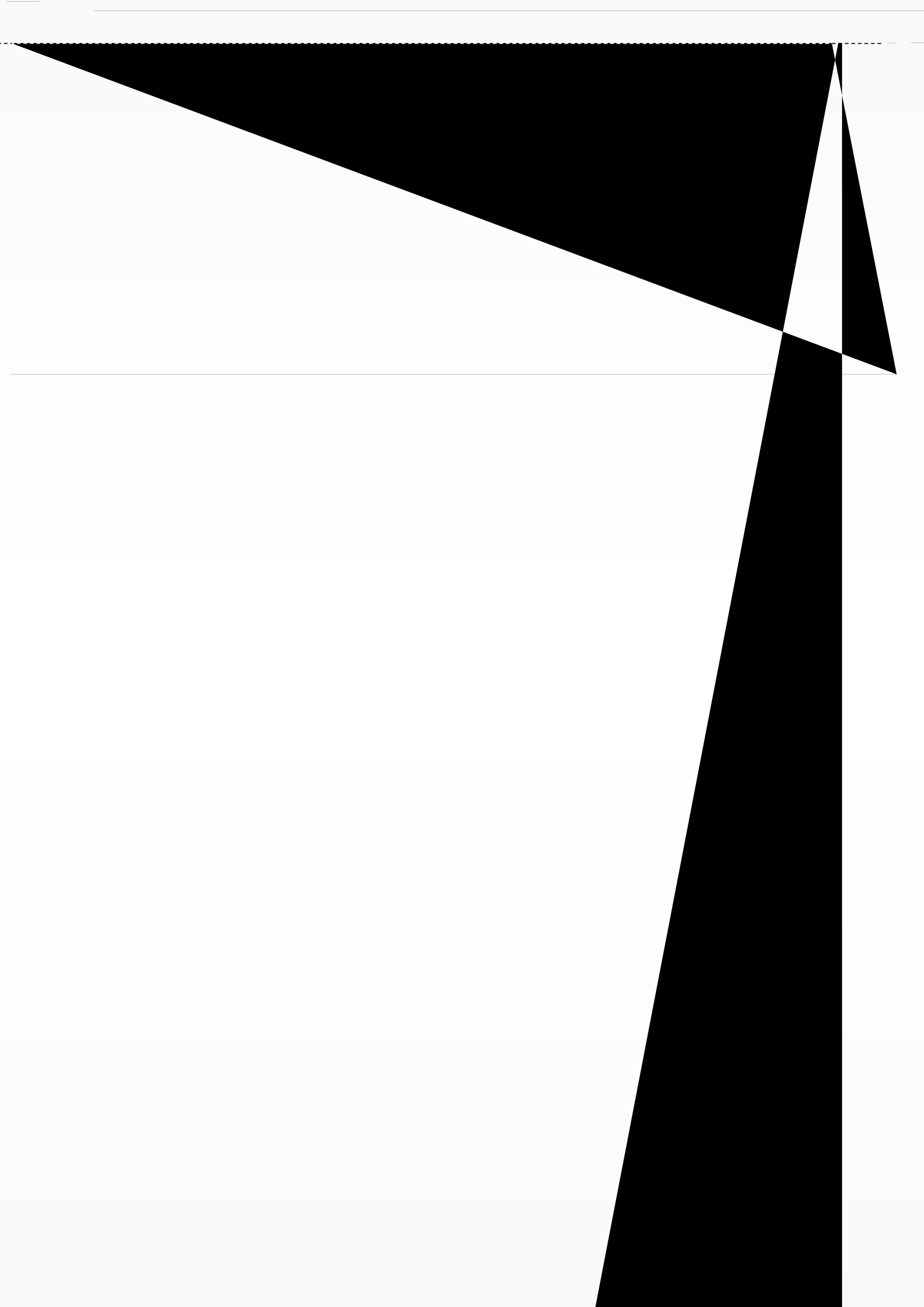
30E

1

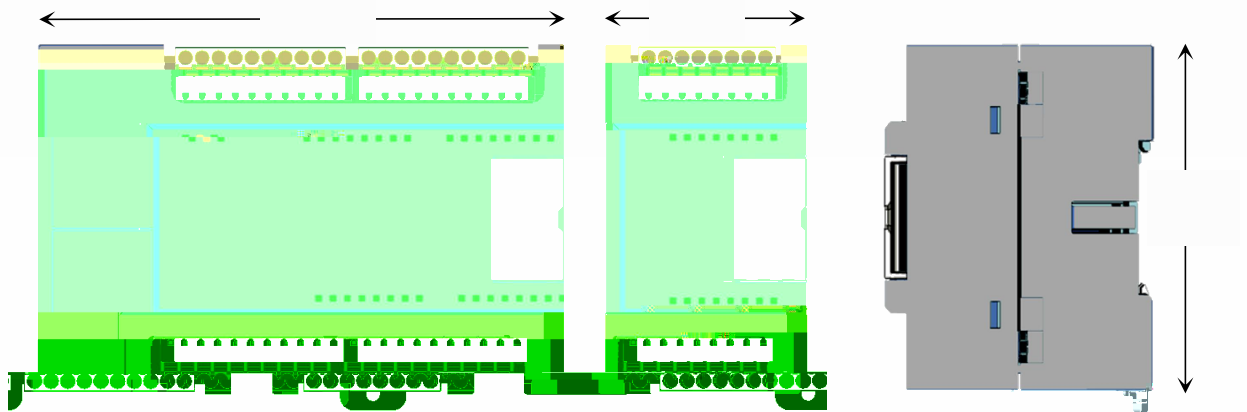
2

9 (0 5 7 3 0 5 0 - e



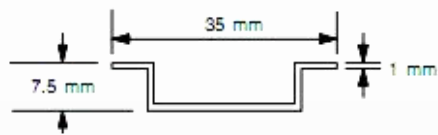


3.1



1

3.2

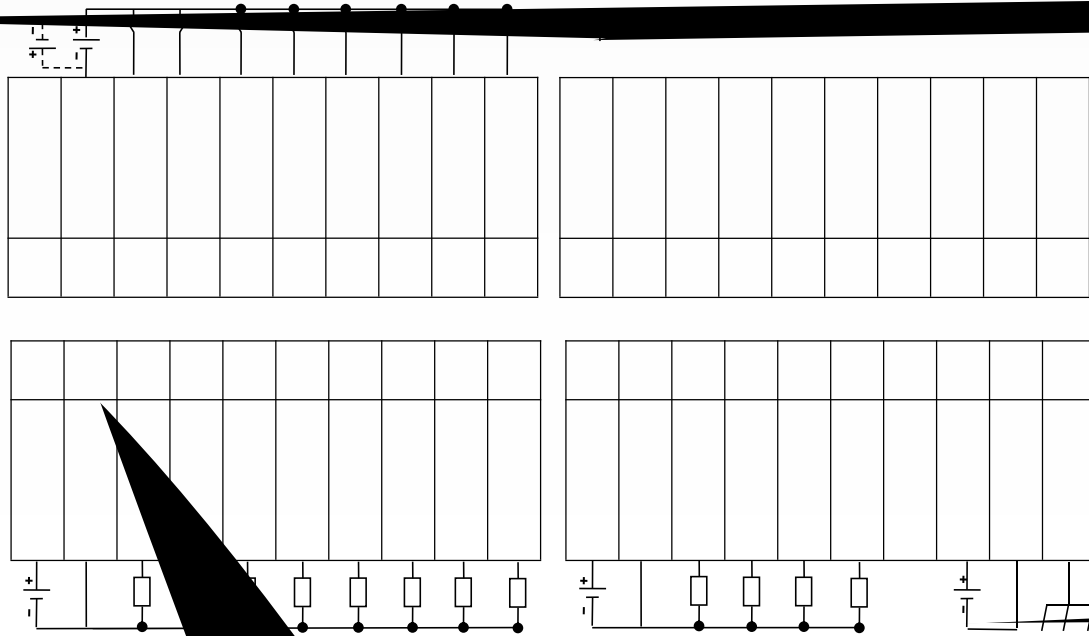


2 DIN

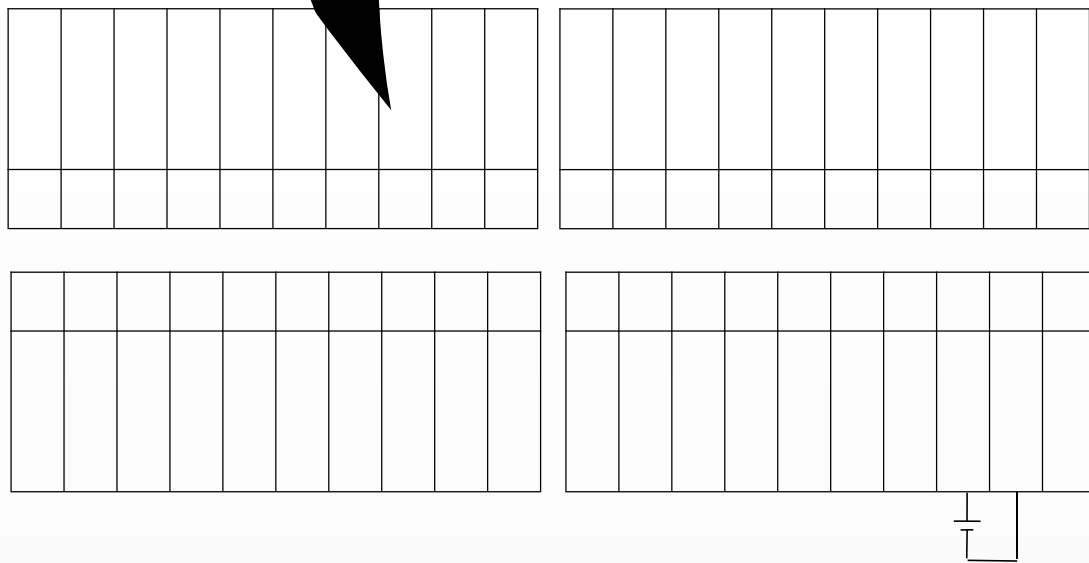


4.

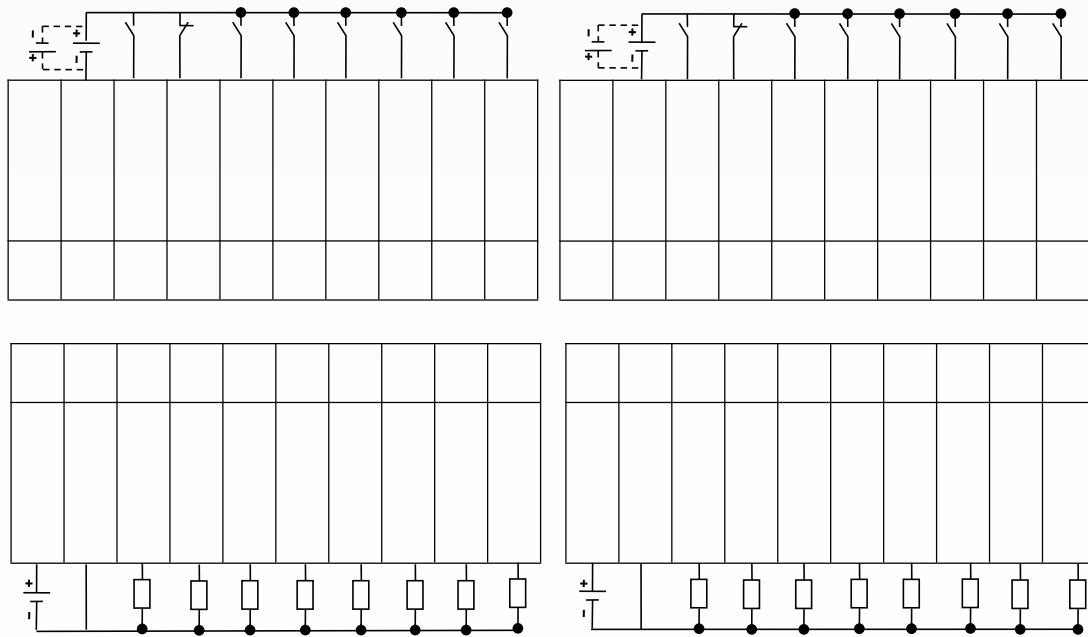
4.1 R51CX-EA



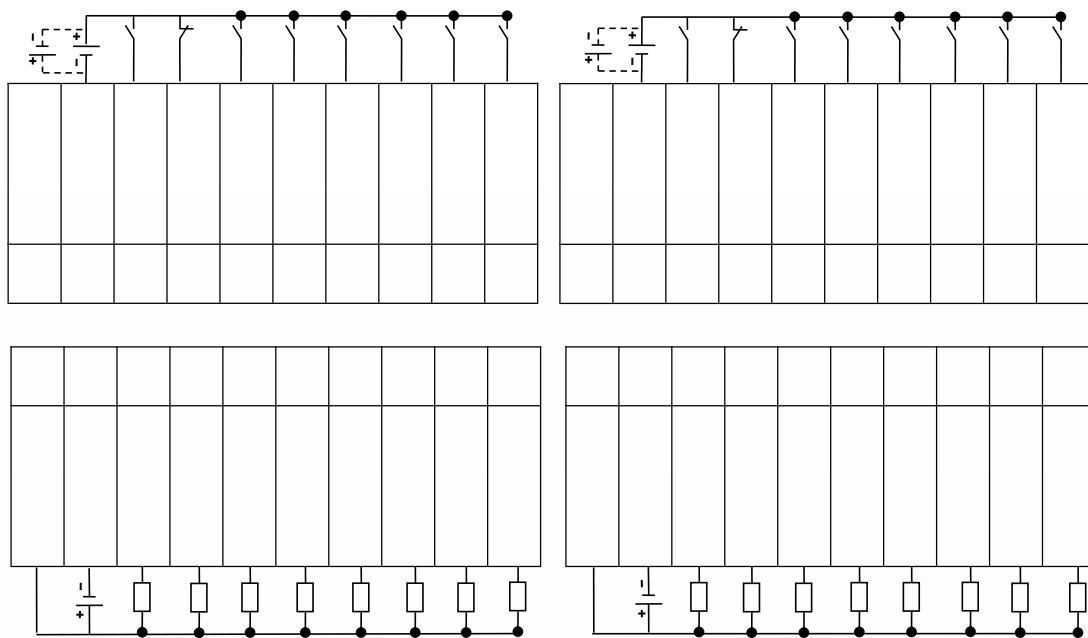
R51C2-EA/XX

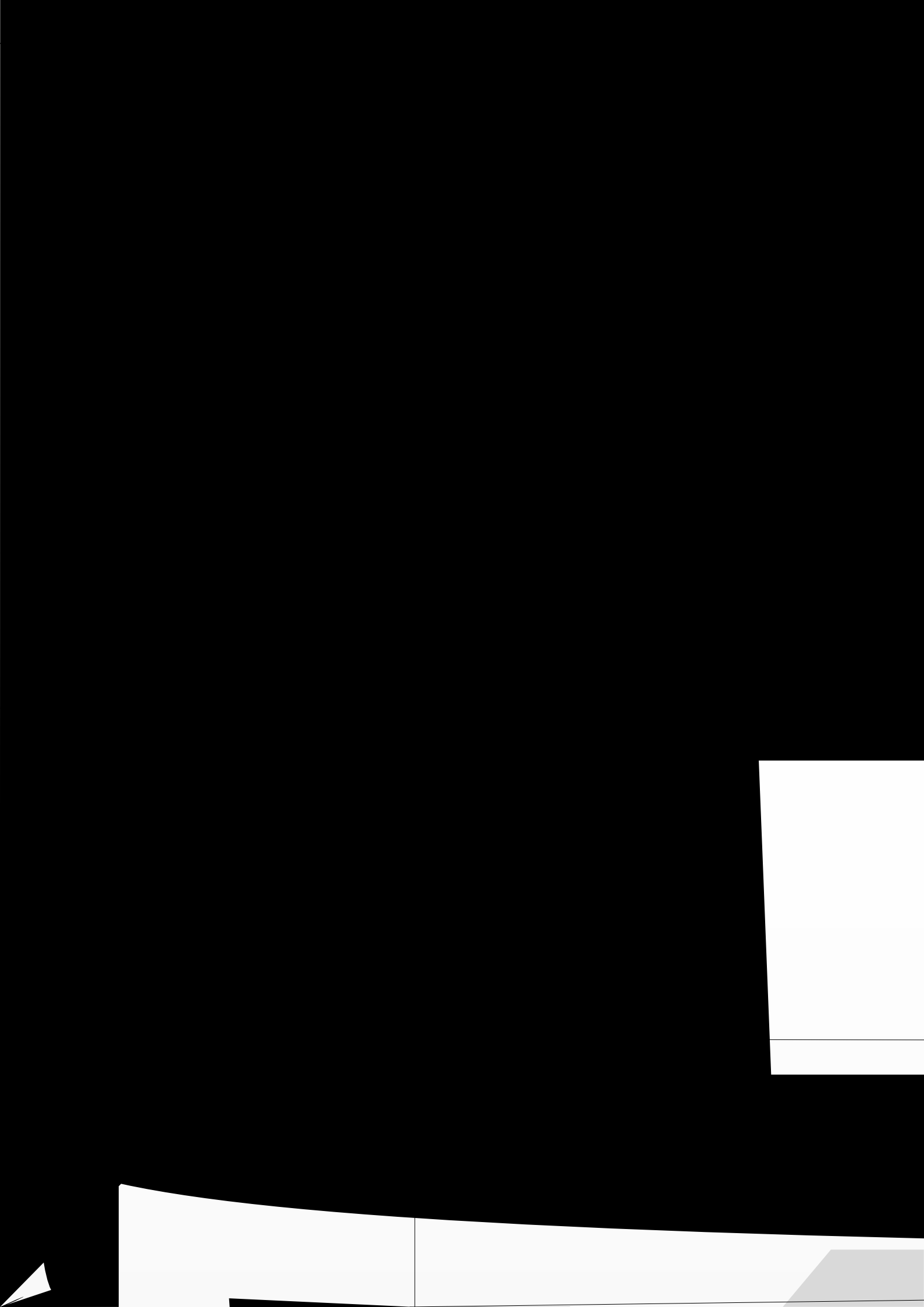


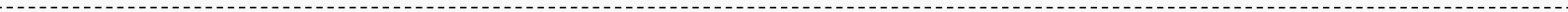
R5201-EA/XX



R5202-EA/XX

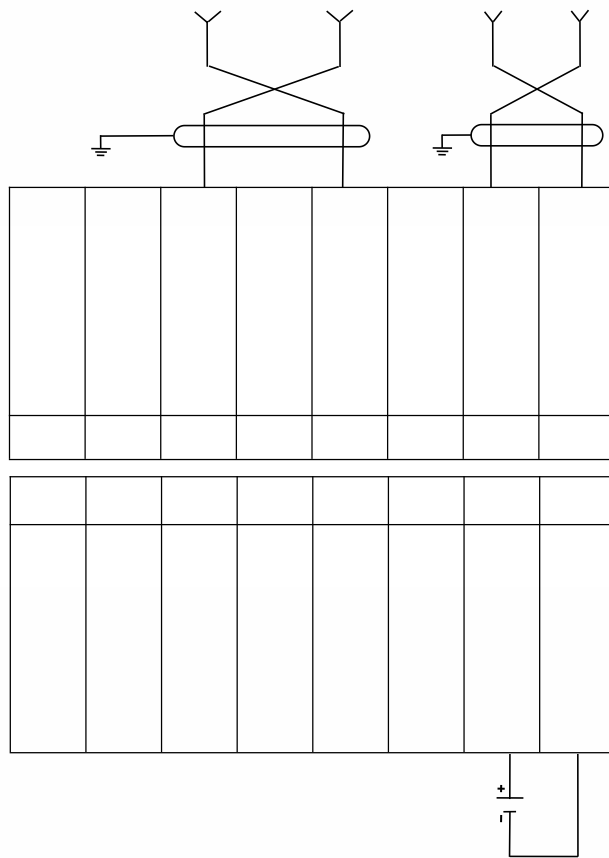






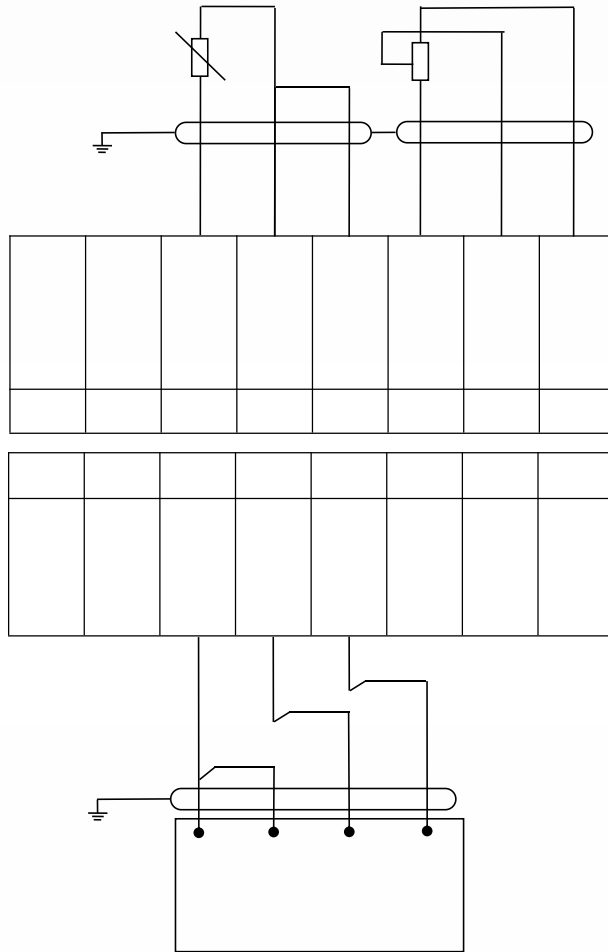


4 E4041

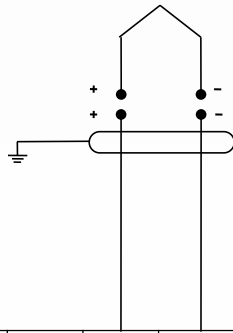


E8041

4



4 E8042

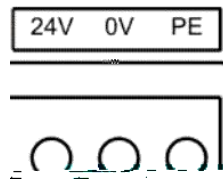


4.5.3 RJ45

LINK1/LINK2	ACT1/ACT2	
○		
●		
	○	
	●	

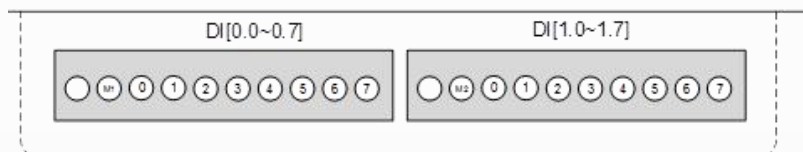
9 RJ45

4.6



10

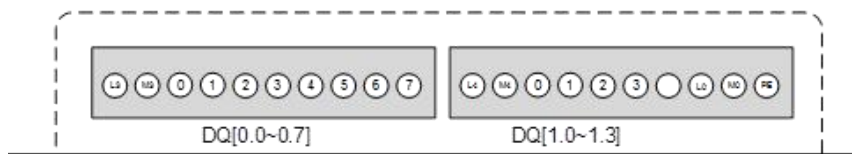
4.7



Digital Input-0		Digital Input-1	

11

4.8



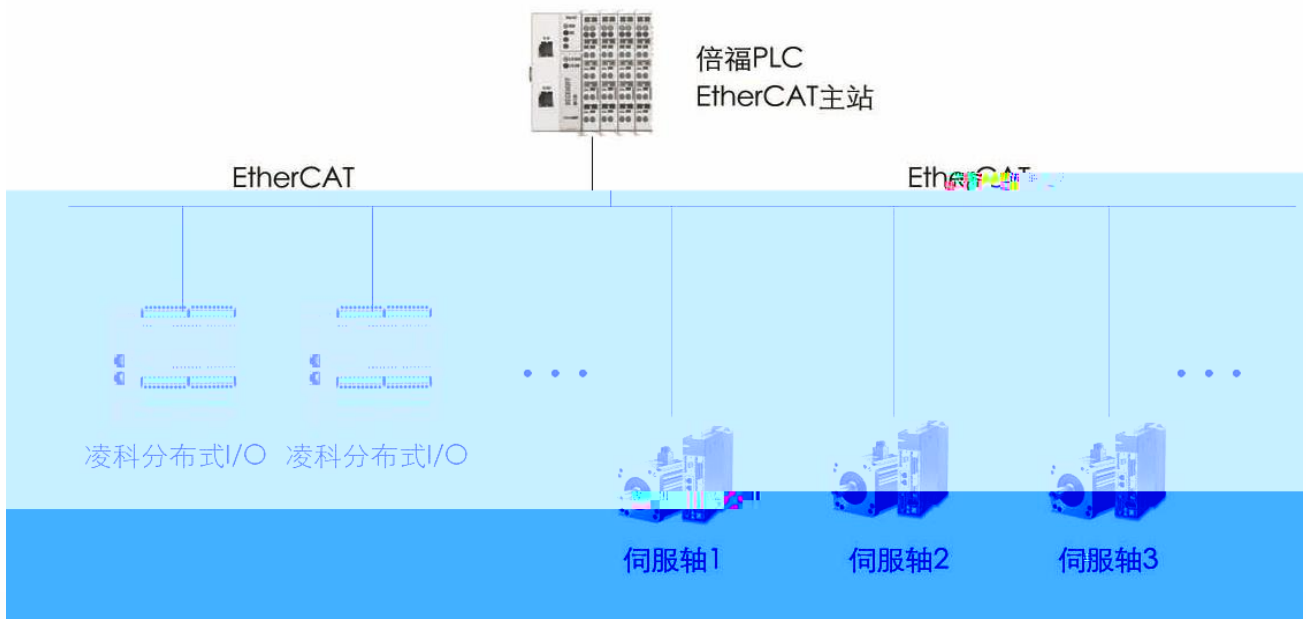
Digital Output -0 Digital Output -1

8B
 >=q 0 1 2 3 4 5 6 7
 q 1 0 p
 0V



5.ETHERCAT

5.1 ETHERCAT IO

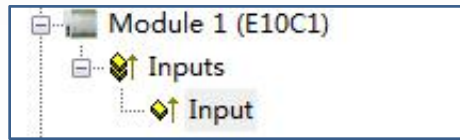


3:ETHERCAT IO

5.2

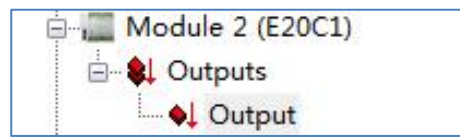
5.2.1 R51C1-EA/F R51C1-EA/Pro

5.3.1 DI



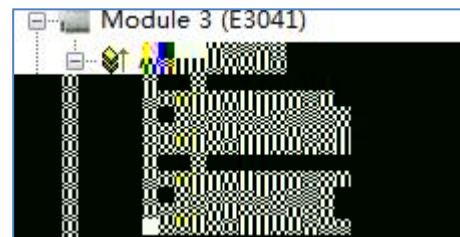
5 E10C1

5.3.2 DQ



6 E20C1

5.3.3 AI



7 E3041

+8010:0	Param_list	RO
+8011:0	Param_Offset	RO
+8012:0	Param_Gain	RO
Param_AverageNum	RO	8013:0

8 E3041 SDO 8010

8010:0	Param_list	RO	
8010:01	CH1_Range	RW P	---
8010:02	CH2_Range	RW P	---
8010:03	CH3_Range	RW P	---
8010:04	CH4_Range	RW P	---

9

13

8011:0	Param_Offset	RO	
8011:01	CH1_Offset	RW P	---
8011:02	CH2_Offset	RW P	---
8011:03	CH3_Offset	RW P	---
8011:04	CH4_Offset	RW P	---

10

8012:0	Param_Gain	RO	
8012:01	CH1_Gain	RW P	---
8012:02	CH2_Gain	RW P	---
8012:03	CH3_Gain	RW P	---
8012:04	CH4_Gain	RW P	---

11

8013:0	Param_AverageNum	RO	
8013:01	CH1_AverageNum	RW P	---
8013:02	CH2_AverageNum	RW P	---
8013:03	CH3_AverageNum	RW P	---
8013:04	CH4_AverageNum	RW P	---

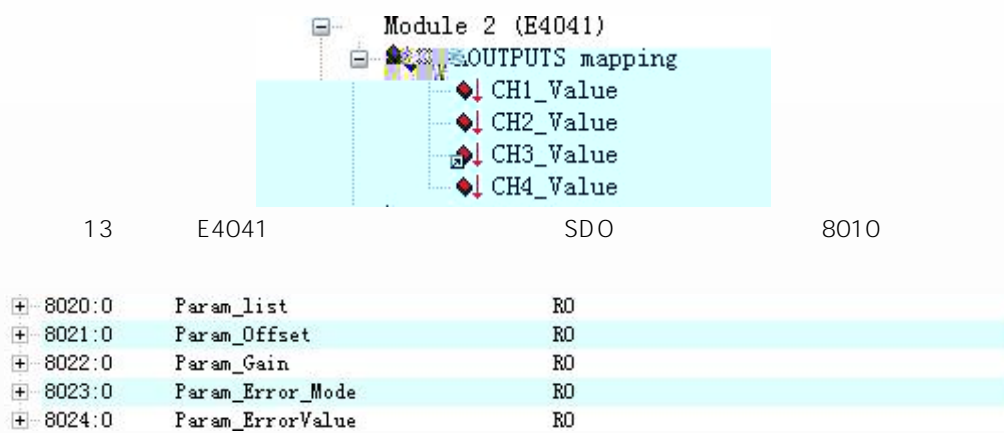
12

5.4

14

5.4.1

AO




```

8020:0 Param_list RO
  8020:01 CH1_Range RW P ---
  8020:02 CH2_Range RW P ---
  8020:03 CH3_Range RW P ---
  8020:04 CH4_Range RW P ---
15 SDO 8010

```


15

```

8021:0 Param_Offset RO
  8021:01 CH1_Offset RW P --- 8021:02
  8021:02 CH2_Offset RW P --- 8021:03
  8021:03 CH3_Offset RW P --- 8021:04
  8021:04 CH4_Offset RW P ---

```

16

```

8022:0 Param_Gain RO
  8022:01 CH1_Gain RW P ---
  8022:02 CH2_Gain RW P ---
  8022:03 CH3_Gain RW P ---
  8022:04 CH4_Gain RW P ---

```

17

5.5

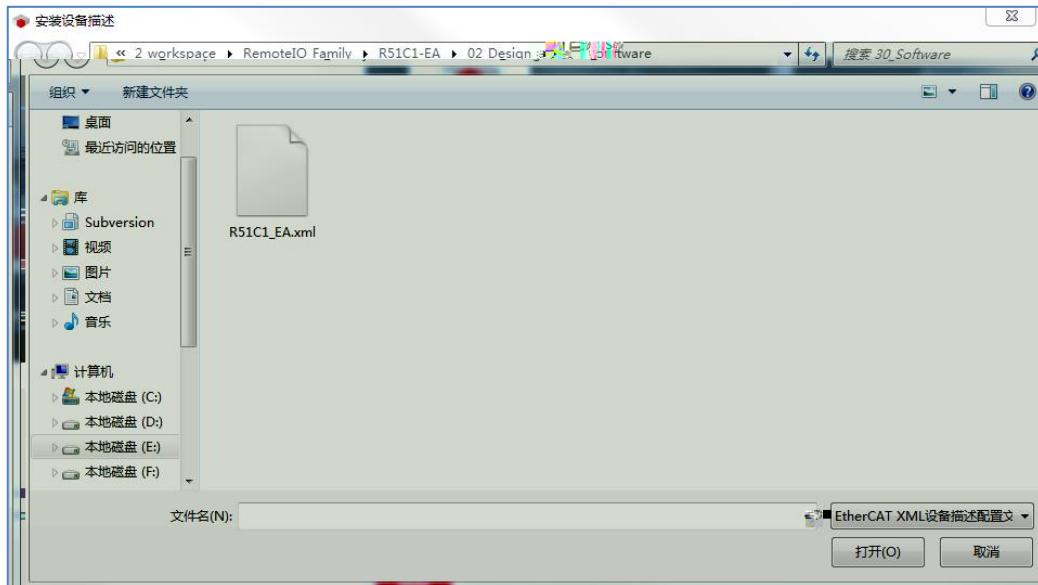
5.6

XML

5.6.1

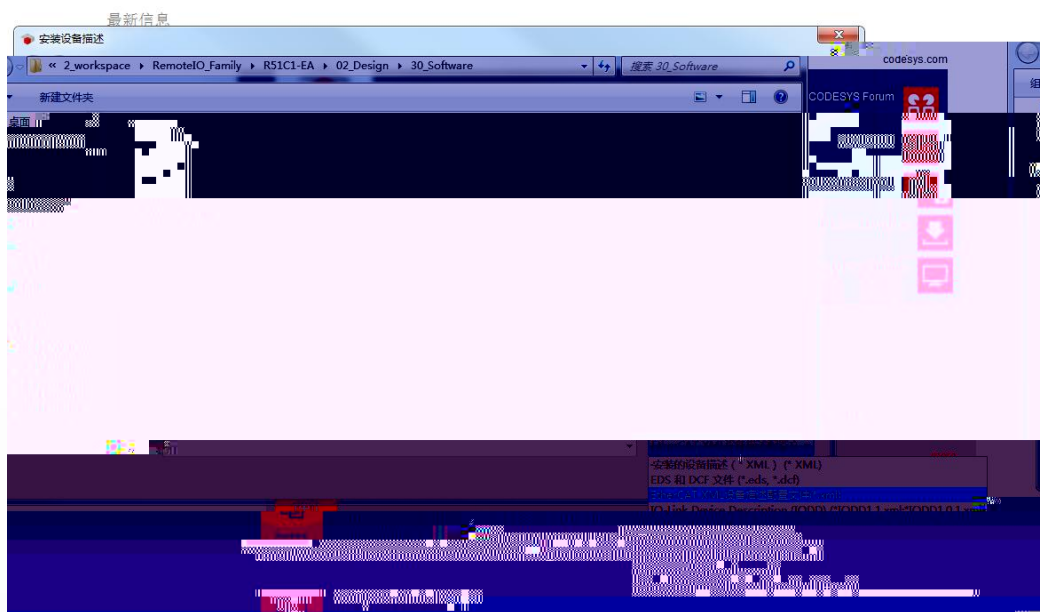
Twincat2

CODESYS V3.5 SP6 Patch 1



18 CODESYS

1



19CODESYS

1



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